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Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Alexander Peleg, et al.

Application No. 09/657,448

Filed: September 8, 2000

For: MICROPROCESSOR HAVING  
NOVEL OPERATIONS

Examiner: K. Kim

Art Unit: 2183

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Technology Center 2100

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OCT 19 2001

**AMENDMENT AND RESPONSE TO RESTRICTION REQUIREMENT**

Sir:

Applicants respectfully requests that the above-identified application be amended as follows:

**IN THE CLAIMS:**

Please add the following new claims:

24. (New) The apparatus of Claim 15, the unpack instruction having an Intel integer opcode format comprising three bytes, a third byte of the three bytes permitting a first source register address and a second source-destination register address.

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